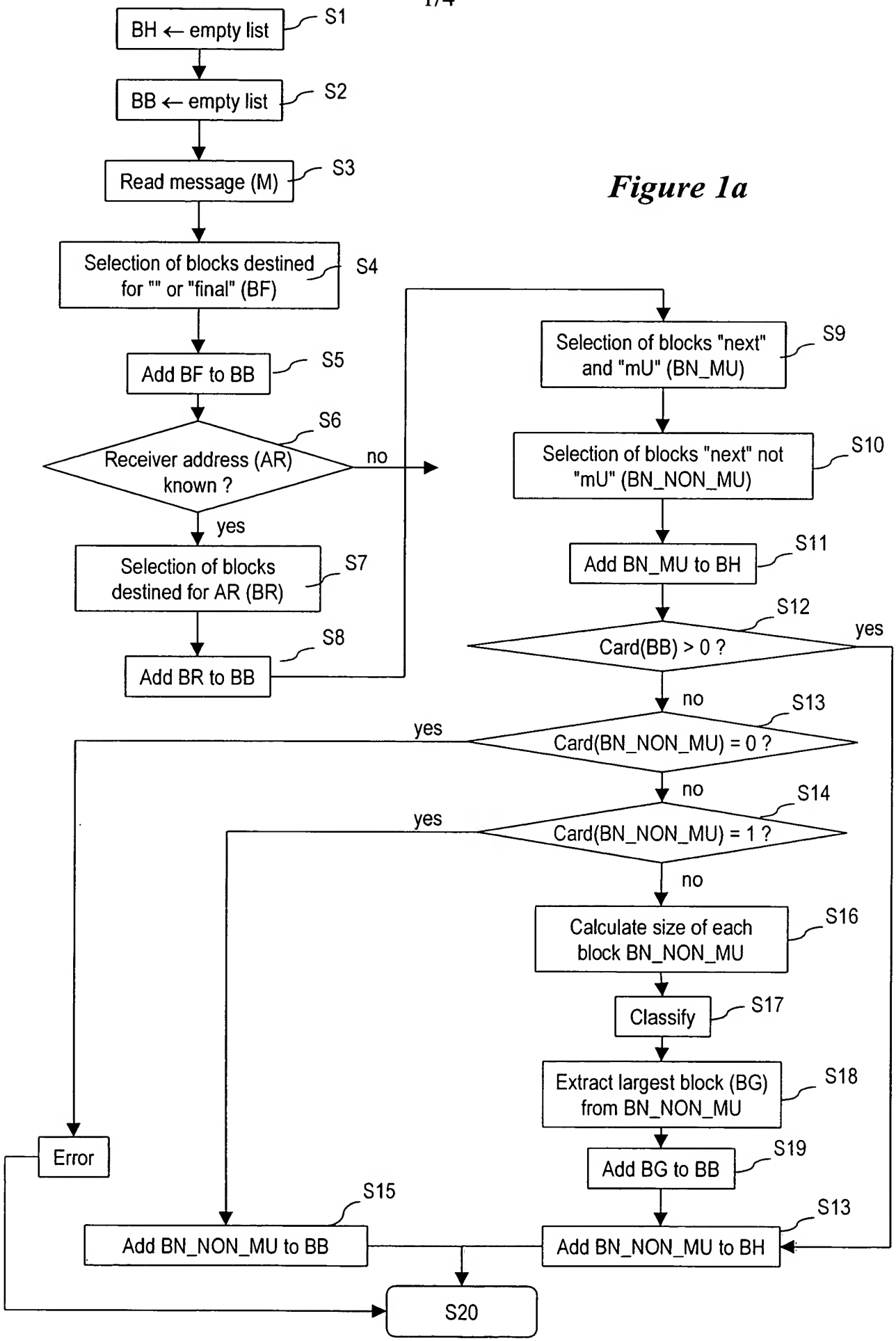
BH ← empty list
S1
BB ← empty list
S2
Read message (M)
S3
Selection of blocks destined for "" or "final" (BF)
S4
Add BF to BB
S5
Receiver address (AR) known ?
S6
no
yes
Selection of blocks destined for AR (BR)
S7
Add BR to BB
S8
Selection of blocks "next" and "mU" (BN_MU)
S9
Selection of blocks "next" not "mU" (BN_NON_MU)
S10
Add BN_MU to BH
S11
Card(BB) > 0 ?
S12
yes
no
Card(BN_NON_MU) = 0 ?
S13
yes
no
Card(BN_NON_MU) = 1 ?
S14
yes
no
Calculate size of each block BN_NON_MU
S16
Classify
S17
Extract largest block (BG) from BN_NON_MU
S18
Add BG to BB
S19
Error
Add BN_NON_MU to BB
S15
Add BN_NON_MU to BH
S13
S20

Figure 1a

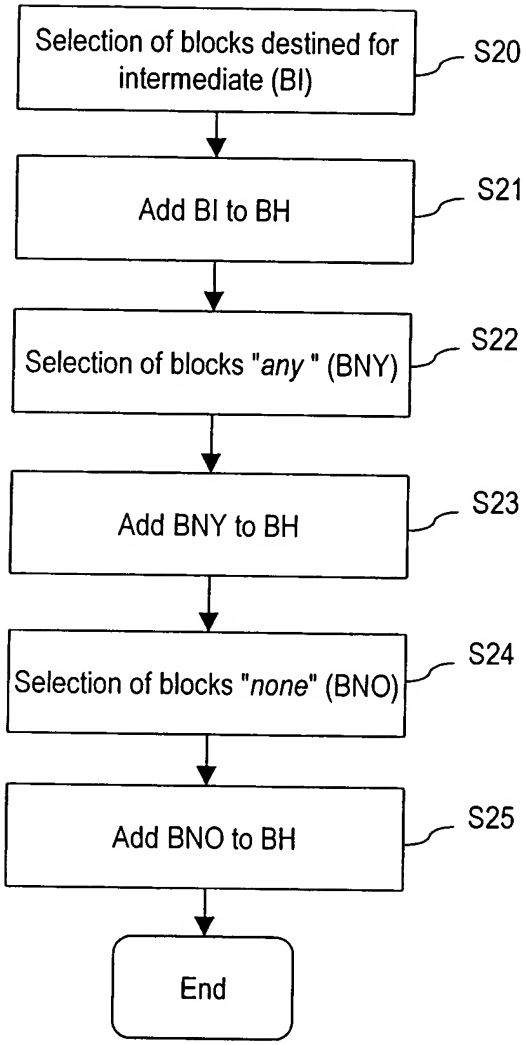
Selection of blocks destined for intermediate (BI)
S20
Add BI to BH
S21
Selection of blocks "*any* " (BNY)
S22
Add BNY to BH
S23
Selection of blocks "*none*" (BNO)
S24
Add BNO to BH
S25
End

Figure 1b

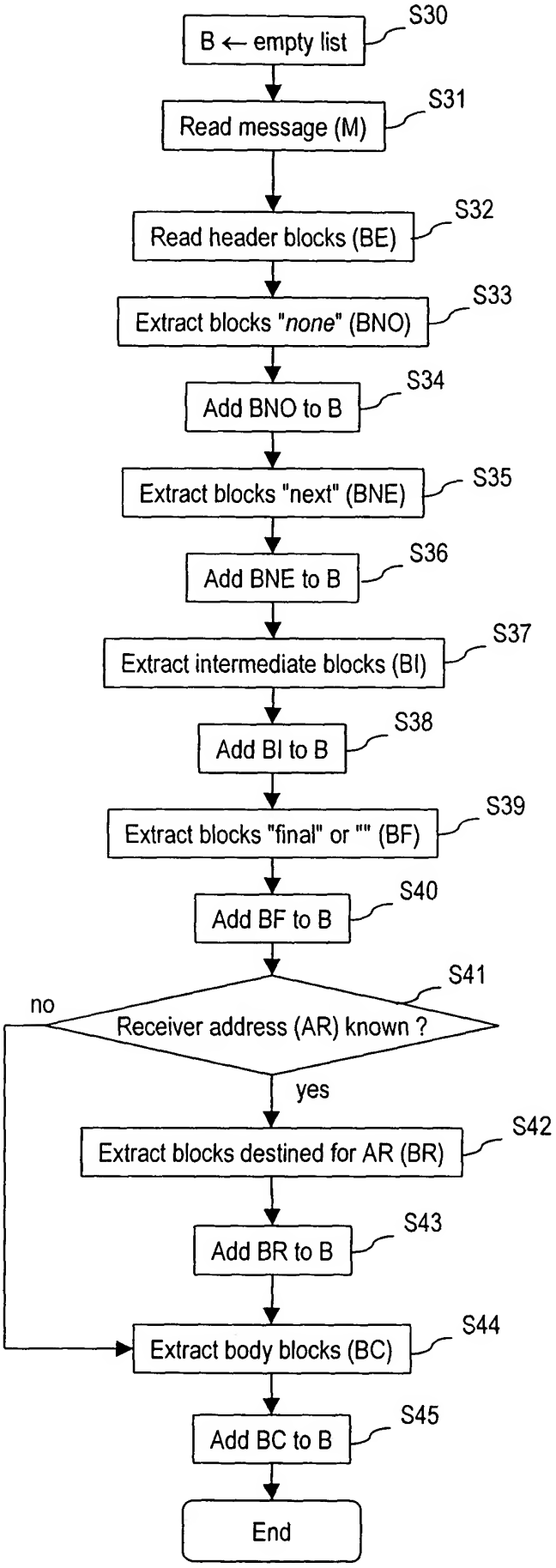
B ← empty list
S30
Read message (M)
S31
Read header blocks (BE)
S32
Extract blocks "*none*" (BNO)
S33
Add BNO to B
S34
Extract blocks "next" (BNE)
S35
Add BNE to B
S36
Extract intermediate blocks (BI)
S37
Add BI to B
S38
Extract blocks "final" or "" (BF)
S39
Add BF to B
S40
Receiver address (AR) known ?
S41
no
yes
Extract blocks destined for AR (BR)
S42
Add BR to B
S43
Extract body blocks (BC)
S44
Add BC to B
S45
End

Figure 2

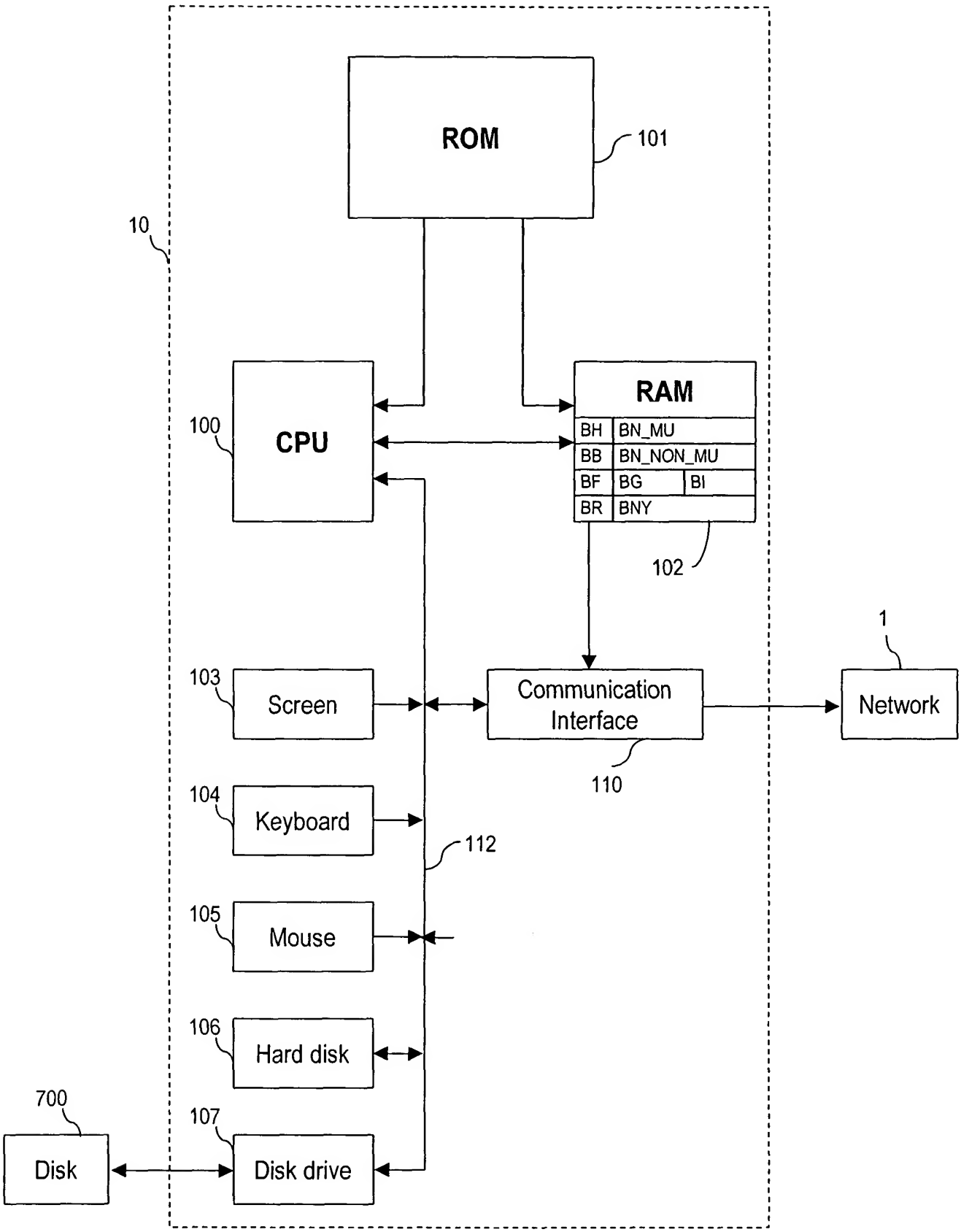
ROM
101
10
CPU
100
RAM
BH
BN_MU
BB
BN_NON_MU
BF
BG
BI
BR
BNY
102
1
103
Screen
Communication Interface
Network
110
104
Keyboard
112
105
Mouse
106
Hard disk
700
107
Disk
Disk drive

Figure 3